



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.09/388,857
Filing Date September 1, 1999
Inventor.....Luan C. Tran
Assignee.....Micron Technology, Inc.
Group Art Unit.....2813
Examiner Laura M. Schillinger
Attorney's Docket No.MI22-878
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And
Related Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the references listed on the attached Form PTO-1449. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art references, if any, are attached. No admission is made regarding whether the submitted references are prior art.

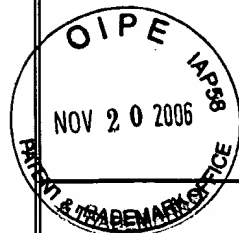
Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 11-20-06

D. Brent Kenady
Reg. No. 40,045
Customer No. 021567

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
MI22-878SERIAL NO.
09/388,857APPLICANT
Luan C. TranFILING DATE
September 7, 2006GROUP
2813LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,573,548	06/03/03	Leung, et al.			
	AB	6,509,595	01/21/03	Leung, et al.			
	AC	6,642,098	11/04/03	Leung, et al.			
	AD	6,744,676	06/01/04	Leung, et al.			
	AE	6,784,048	08/31/04	Leung, et al.			
	AF	6,902,975	06/07/05	Tu			
	AG						
	AH	2002/0053691	05/09/02	Leung, et al.			
	AI	2001/0052610	12/20/01	Leung, et al.			
	AJ	2003/0001181	01/02/03	Leung, et al.			
	AK	2003/0151071	08/14/03	Leung, et al.			
	AL	2003/0151072	08/14/03	Leung, et al.			
	AM	2004/0207100	10/21/04	Madurawe			
	AN						
	AO						
	AP						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AQ							
	AR							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AS		

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.